

PATENT NUMBER 4,111,111

ISSUE CLASSIFICATION	
Class	Subclass

<p>O.I.P.E.</p> <p>SCANNED <i>Go</i> O.A. <i>AA</i></p>	<p>PATENT DATE</p>
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APPLICATION NO. 09/942461	CONT/PRIOR F	CLASS 205 156	SUBCLASS 89.12	ART UNIT 132 1734	EXAMINER MAYES
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Sadaaki Sakamoto

Multilayered ceramic board, method for fabricating the same, and electronic device using multilayered ceramic board

PTO-2040
12/89

ISSUING CLASSIFICATION							
ORIGINAL				CROSS REFERENCE(S)			
CLASS	SUBCLASS			CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
INTERNATIONAL CLASSIFICATION							

☐ Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED	
	_____ (Primary Examiner) (Date)		ISSUE FEE	
			Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		ISSUE BATCH NUMBER	

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